

# Growth of III-V nanowires: from fundamental physics to device applications

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The catalyst-assisted growth of semiconductor nanowires (NWs) has emerged as a flexible and promising technology for the synthesis of self-assembled nanostructures with potential impact on the investigation of fundamental properties and on the development of innovative devices. Different materials can be easily combined in individual high-quality single crystal nanowires without lattice-matching constrains. Axial and radial heterostructured nanowires have been realized combining semiconductor materials with suitable physical properties. As a consequence, nanowire technology represents a powerful research and development platform for fundamental physics investigations as well as for innovative device applications in nanoelectronics [1], optoelectronics [2] and energy harvesting [3]. In the first part of my presentation I will show examples for the realization of axial and radial heterostructured nanowires with controlled morphology and crystal structure (see Fig.1). Furthermore, I will show that InAs/InP NW-based single-electron devices have a great potential and allow an extreme and reliable control of electron filling down to the last free electron, even if energy spectrum and coupling are usually harder to tune [4-5]. In the third part of the presentation I will show the use of nanowire technology in the development of thermoelectric (TE) devices. I will show recent results on thermovoltages in single InAs nanowire (NW) field effect transistors [3].

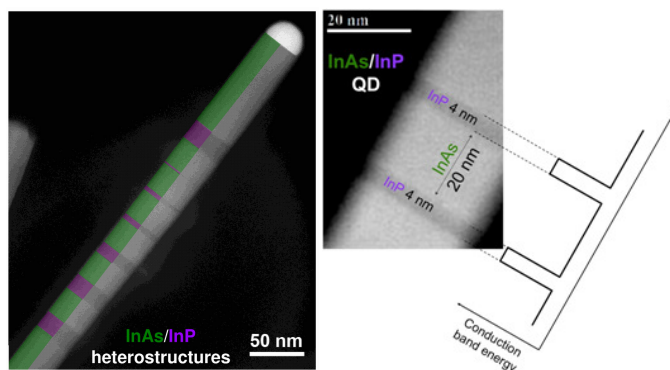


Fig.1 STEM and TEM micrographs of InAs/InP NW heterostructures

## References:

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